

ABSTRACT OF THE DISCLOSURE

First and second line memories alternately store
input video signals for every scanning line. The video
signals stored in the line memories are read by a
5 predetermined number of times in accordance with a
signal system of the input video signals. A calculator
calculates average values among the video signals read
from the line memories. A selector circuit
sequentially selects the video signals read from the
10 first and second line memories when the input video
signals are based on an interlace system, and
sequentially selects the video signals read from the
first and second line memories and the averaged video
signals when the input video signals are based on a
15 progressive scan system. The selected signals are
supplied to a liquid crystal display apparatus.